2818

DEC 0 9 2004

First Named Inventor
Serial No.

10/017,892

Filing Date
December 12, 2001

Group Art Unit
2818

Examiner Name
Connie C. Yoha

Confirmation No.
9026

Attorney Docket No.
400.094US01

GENERAL TRANSMITTAL FORM UNDER 37 CFR 1.8 (LARGE ENTITY)

Title: METHOD AND ARCHITECTURE TO CALIBRATE READ OPERATIONS IN SYNCHRONOUS FLASH MEMORY

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Enclosures

The following documents are enclosed:

X Examiner Interview Summary (2 pgs.);

X An itemized return-receipt postcard

Leffert Jay & Polglaze, P.A. P.O. Box 581009 Minneapolis, MN 55458-1009 T – 612/312-2200

F - 612/312-2250

Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.

CUSTOMER NO. 27073

Submitted By							
Name	Andrew C. Walseth Ro	eg. No. 4.	3,234	Telephone	(612) 312-2207		
Signature	e Mushan Chahat	4		Date	12/6/04		
Certificate of Mailing							
I certify that this correspondence and the identified documents listed on this transmittal are being deposited with							
	ed States Postal Service with sufficient						
Commiss	sioner for Patents, P. O. Box 1450, Ale	exandria, VA	. 22313-1450	on December	6, 2004.		
Name	Charlene M Burt	Signature	Ma	l'Th	3		

(LARGE ENTITY TRANSMITTAL UNDER 37 CFR § 1.8)

First Named Inventor	Frankie F. Roohparvar		
Serial No.	10/017,892		
Filing Date	December 12, 2001		
Group Art Unit	2818		
Examiner Name	Connie C. Yoha		
Confirmation No.	9026		
Attorney Docket No.	400.094US01		

EXAMINER INTERVIEW SUMMARY

Title: METHOD AND ARCHITECTURE TO CALIBRATE READ OPERATIONS IN SYNCHRONOUS FLASH MEMORY

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

In a telephonic conversation with the Examiner on October 7, 2004, with the below-signed attorney, Andrew C. Walseth, the Examiner and Applicant discussed the Restriction Requirement of September 10, 2003 and the Supplemental Response to Restriction Requirement of August 12, 2004, selecting Group I (claims 1-4) for examination and affirming cancellation of Group II (claims 14-19) of the Present Application. The Examiner and Applicant specifically discussed the patentability of pending claims 1-4 over Honda et al. (U. S. Patent No. 6,377,502), which was cited in a Divisional Application (U.S. Application No. 10/375,160, which issued as U.S. Patent No. 6,735,122 on May 11, 2004) and indicated by the Examiner to also be applicable to the Present Application. The Examiner and the Applicant agreed that Honda et al. does not teach or disclose either a first or second read circuits having an adjustable sense amplifier circuit. The Examiner indicated that claims 1-4 would be allowable if they were amended to specifically claim an adjustable sense amplifier. The Examiner and Applicant agreed to an Examiner's amendment that amends claim 1 to include the limitations of dependent claim 3 and canceling claim 3 to reflect this understanding. The Examiner indicated that she would review the Present Application and issue an Office Action or a Notice of Allowance based on this Examiner's Amendment.

Applicant believes the foregoing Summary accurately reflects the substance and scope of the telephonic interview of Thursday October 7, 2004. Applicant requests notification if the Examiner disagrees with the accuracy or completeness of the Summary.

EXAMINER INTERVIEW SUMMARY

PAGE 2

Serial No. 10/017,892

Attorney Docket No. 400.094US01 Title: METHOD AND ARCHITECTURE TO CALIBRATE READ OPERATIONS IN SYNCHRONOUS

FLASH MEMORY

The Examiner is invited to contact Applicant's Representatives at direct dial (612) 312-2207 if there are any changes or questions regarding this Examiner Interview Summary or if prosecution of this application may be assisted thereby.

Respectfully submitted,

Date: 12/6/04

Andrew C. Walseth Reg. No. 43,234

Attorneys for Applicant Leffert, Jay & Polglaze, PA P.O. Box 581009 Minneapolis, MN 55458-1009

telephone: (612) 312-2207 facsimile: (612) 312-2250